



Jaihind Comprehensive Educational Institute's
Jaihind College of Engineering, Kuran

Affiliated to Savitribai Phule Pune University

DTE :- EN6609 SPPU :- CEGP015730



Hon'ble Late Shri Tatyasaheb Gunjal
Founder-President

Ref No. JCEI/JCOE/

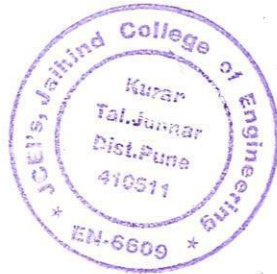
Date:-

ACADEMIC YEAR 2024-25

Schedule of activities for first year Post Graduate Engineering admissions against vacant seats after CAP Rounds

| Sr. No. | Activity | Schedule | |
|---------|--|---------------------------|------------|
| | | Start Date | End Date |
| 1 | Advertisement and Call for application and Document Verification in person | 06.09.2024 | 11.09.2024 |
| 2 | Display of provisional merit list | 12.09.2024 at 10.00 am | |
| 3 | Submission of grievances if any | 12.09.2024 up to 11.00 am | |
| 4 | Display of final merit list | 12.09.2024 at 12.00 noon | |
| 5 | Against CAP admission allotment and Reporting to institute | 12.09.2024 up to 05.00 pm | |
| 6 | Cut-Off Date | 12.09.2024 | |

Date: 04.09.2024




Principal
JCEI's Jaihind College Of Engineering
Kuran, Tal. Junnar, Dist. Pune - 410511

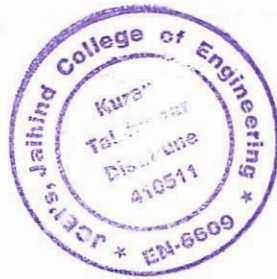
**Seats vacant after CAP rounds for First Year Post Graduate Degree in Engineering Academic Year
2024-25**

Savitribai Phule Pune University
06609 - Jalhind College Of Engineering Kuran

| Course Name | Non-Sponsored | | | | | | | | | | Non-Sponsored PWD | | | | | | | | Non-Sponsored | | | Sponsored | | | Total | | | | |
|--|---------------|------|----|----|------|-----|-----|-----|-----|------|-------------------|----|----|------|-----|-----|-----|-----|---------------|---------------------|--------|-----------|----|-------------|-------|------|----|----|-------------|
| | Choice Code | Open | SC | ST | VJDT | NTB | NTC | NTD | OBC | SEBC | Open | SC | ST | VJDT | NTB | NTC | NTD | OBC | SEBC | PWD Common Reserved | Orphan | IL | MI | Choice Code | | Open | IL | MI | Vacant Seat |
| Computer Engineering | 066024510 | 0 | 1 | 1 | 0 | 0 | 0 | 0 | 2 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 5 |
| Signal Processing | 0660935810 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | |
| Design Engineering | 0660960110 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 2 | 1 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 5 | |
| Artificial Intelligence and Data Science | 0660994310 | 2 | 0 | 1 | 0 | 0 | 0 | 0 | 2 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 7 | |
| Civil - Structural Engineering | 0660921110 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 2 | |
| VLSI Design and Embedded System | 0660938810 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 0 | 1 | |

FOR DETAILS:

<https://me2024.mahacet.org/ViewPublicDocument?MenuId=3372>




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