



JCEI'S
JAIHIND COLLEGE OF ENGINEERING, KURAN
Affiliated to Savitribai Phule Pune University, Pune
AN ISO 14001:2004 EMS & AN ISO:9001:2015 QMS CERTIFIED
INSTITUTE

TIME TABLE
(ACADEMIC YEAR -2019-20)

DEPARTMENT:- Electronics & Telecommunication Engg

CLASS :- S.E. Semester-I		DIV:		SEMESTER-I		W.E.F. :- DATE:15/06/19	
NAME OF CLASS TEACHER:-Mr.Yendhe R.S.							
TIME / DAY	MON	TUE	WED	THU	FRI	BLOCK / CLASS NO:-10	
09:00-10:15	ECM	EDC	SS	DE	SS TUT- S1,S2	SAT	EDC
10:15-11:15	DE	DE	ECM	SS	EDC	ECM	EDC
RECESS							
12:00-01:00	DE-S1	EDC-S1	EDC-S1	ECM-S1	EMIT-S1	SS	SS
1:00-02:00	EDC-S2	EMIT-S2	ECM-S2	DE-S2	DSA-S2	DSA	DSA
RECESS							
02:15-03:15	EDC	SS	DE	EDC	DE	DSA	DSA
03:15-4:15	DSA	DSA	DSA	EMIT	ECM	AC-I (Seminar)	AC-I (Seminar)
Batches :- S1 :-01 To 13 S2-14 Onwards							
Name Of Subject	TH/PR/OR/TW	Abbreviation	Subject Code	CLASS/LAB NO.	Name of Subject Teacher	Guardian for Roll Nos.	
Signal & System	TH/TW	SS	204181	10	Miss.Andre S.B.		
Electronic Devices & Circuits	TH/PR	EDC	204182	10/S006	Mr.Tambe.A.T		
Electrical Circuits & Machine	TH	ECM	204183	10	Mr.Yendhe R.S.	01 To 13	
Electrical Circuits & Machine	TW	ECM	204183	G-09	Mrs.Dhede V.M		
Data Structure & Algorithm	TH/OR	DSA	204184	10/S020	Miss.Gore S.S.		
Digital Electronics	TH/PR	DE	204185	10/S001	Mr.Mane A. B.		
Electronic Measuring Instru. & Tools	TH/TW	EMIT	204186	10/S007	Miss. Shinde S.E		
Audit Course I				10	Mr.Mane A. B.		
ISSUE	REVISION	PREPARED BY	VERIFIED BY	APPROVED BY	PAGE No	14 Onwards	
No. : 00	No. : 00	DSGN.: T T INCHARGE	DSGN.: HOD	DSGN.: PRINCIPAL	CHAPTER No.	LIST-JCOE-01	
DATE:01/07/17	DATE:15/06/19	SIGN.	SIGN.	SIGN.	01 OF 01		

Head of Department

(E & TC) JCEI's Jaihind College of Engineering

Kuran, Tal-Junnar, Dist. Pune - 410511

Kuran, Tal-Junnar, Dist. Pune



Director

Internal Quality Assurance Cell
JCEI's Jaihind College of Engineering
Kuran, Tal-Junnar, Dist. Pune -410511

F/JCOE/AC/01/00/15/06/2019



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 INSTITUTE

TIME TABLE
 (ACADEMIC YEAR -2019- 20)

DEPARTMENT:- Electronics and Telecommunication Engineering

CLASS :- T.E.		DIV:		SEMESTER: I		W.E.F. :- DATE: 15/06/19	
NAME OF CLASS TEACHER:- Ms. Andre S.B.		BLOCK / CLASS NO:- 11					
TIME / DAY	MON	TUE	WED	THU	FRI	SAT	
09:00-10:15	DC-T1	DSP-T1	MECH-T1	MC-T1	ESD-T1	DSP	
10:15-11:15	DSP-T2	MC-T2	ESD-T2	DC-T2	MECH-T2	EM	
12:00-01:00	DC	EM	DSP	DC	MECH	MECH	
1:00-02:00	DSP	MECH	DC	DSP	EM TUT-T1,T2	MC	
RECESS							
02:15-03:15	MECH	DC	EM	ESD	MC	DC	
03:15-4:15	EM	ESD	MC	MC	DSP	AC-III(Seminar)	
Batches :- T1 :-01 To 15 T2-16 onwards							
Name Of Subject	TH/PR/OR/TW	Abbreviation	Subject Code	CLASS /LAB No.	Name of Subject Teacher	Guardian for Roll Nos.	
Digital Communication	TH	DC	304181	11	Miss. Shinde S.E.		
Digital Signal Processing	TH	DSP	304182	11	Miss.Andre S.B.		
Electromagnetics	TH	EM	304183	11	Mr.Choure A.R.		
Microcontrollers	TH	MC	304184	11	Mr.Gadekar S.R.		
Mechanics	TH	MECH	304185	11	Mr.Mane A. B.		
Electronics System Design	TH/OR	ESD	304193	11/S001	Mrs.Dhede V.M		
Signal Processing & Communications Lab	PR/TW	SPCL	304191	S002/S022	Miss. Shinde S.E. & Miss.Andre S.B.		
Microcontrollers & Mechatronics Lab	PR/TW	MML	304192	S020	MrMane A.B. & Mr.Gadekar S.R.		
Audit Course III				11	Mr.Choure A.R.	01 to 15	
					Mr.Tambe.A.T	16 onwards	
					Mr.Mane A. B.	CHAPTER No.	
ISSUE	REVISION	PREPARED BY	VERIFIED BY	APPROVED BY	PAGE No	CHAPTER No.	
No. : 00	No. : 00	DSGN.: T T INCHARGE	DSGN.: HOD	DSGN.: PRINCIPAL	01 OF 01	LIST-JCOE-01	
DATE:01/07/17	DATE:15/06/19	SIGN. <i>(Signature)</i>	SIGN. <i>(Signature)</i>	SIGN. <i>(Signature)</i>			

F/JCOE/AC/01/00/15/06/2019

Head of Department *(Signature)* **Principal**

(E & T) JCEI's Jaithind College of Engineering
 Kuran, Tal-Junnar, Dist Pune - 410511

Director
 Internal Quality Assurance Cell
 JCEI's Jaithind College of Engineering
 Kuran, Tal.Junnar, Dist.Pune -410511



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TIME TABLE
(ACADEMIC YEAR 2019 -20)

CLASS :- B.E.

INSTITUTE

SEMESTER: I

DEPARTMENT:- Electronics and Telecommunication Engineering

NAME OF CLASS TEACHER:- Mr. Gadekar S.R.

DIV:

W.E.F. :- DATE:15/06/19

TIME / DAY	MON	TUE	WED	THU	FRI	SAT
09.00-10:15	EPD/AI	VLSI	CNS	VLSI	EPD/AI	IOT
10:15-11:15	CNS	IOT	IOT	RMT	IOT	EPD/AI

RECESS						
TIME	VLSI	CNS	VLSI	EPD/AI	RMT	RMT
12.00-01:00	VLSI	CNS	VLSI	EPD/AI	RMT	RMT
1.00-02:00	RMT	RMT	EPD/AI	AC-V(Seminar)	CNS	CNS

RECESS						
TIME	PP I-B1	CNS-B1	RMT-B1	VLSI-B1	IOT-B1	VLSI
02:15-03:15	PP I-B1	CNS-B1	RMT-B1	VLSI-B1	IOT-B1	VLSI
03:15-4:15	IOT-B2	VLSI-B2	CNS-B2	PP I-B2	RMT-B2	Seminar

Batches :- B1 :-01 To 15 B2:-16 onwards

Name Of Subject	TH/PR/OR/ TW	Abbreviation	Subject Code	CLASS/Lab No.	Name of Subject Teacher	Guardian for Roll Nos.
VLSI Design & Technique	TH	VLSI	404181	12	Mr.Choure A.R.	
Computer Networks & Security	TH	CNS	404182	12	Mr.Tambe A.T.	
Radiation & Microwave Techniques	TH	RMT	404183	12	Mrs.Dhede V.M	16 onwards
Internet of Things	TH	IOT (EL-I)	404184	12	Miss.Shinde S.E.	
Electronic Product Design/Artificial Intelligence	TH	EPD/AI(EL-II)	404185	12	Mr.Gadekar S.R.	
Lab Practice-I (CNS+RMT)	OR/TW	LP-I	404186	S020/S002	Mr.Tambe A.T./Mrs.Dhede V.M	
Lab Practice-II (VLSI+IOT(EL-I))	PR/TW	LP-II	404187	S022/S020	Mr.Choure A.R./Miss.Shinde S.E.	
Project Phase -I	OR	PP-I	404188	S022	Miss.Andre S.B.	1 to 15
Audit Course V				12	Mr.Tambe A.T.	
ISSUE	REVISION	PREPARED BY	VERIFIED BY	APPROVED BY	CHAPTER No.	
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DATE:01/07/17	DATE:15/06/19	SIGN. <i>[Signature]</i>	SIGN. <i>[Signature]</i>	SIGN. <i>[Signature]</i>		

F/JCOE/AC/01/00/15/06/2019

Head of Department
JCEI's Jaithind College of Engineering
(E & TC) JCEI's Jaithind College of Engineering
Karan, Tal. Jambhat, Dist. Pune



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