



**JCEI'S
JAIHIND COLLEGE OF ENGINEERING, KURAN**
Affiliated to Savitribai Phule Pune University, Pune
AN ISO 14001:2004 EMS & An ISO:9001:2015 QMS CERTIFIED
INSTITUTE

**TIME TABLE
(ACADEMIC YEAR -2020-21)**

DEPARTMENT:- Electronics & Telecommunication Engg

CLASS :- S.E. Semester-I		DIV:		SEMESTER: I		W.E.F. :- DATE: 06/07/20	
NAME OF CLASS TEACHER:- Mr. Tambe A.T.				BLOCK / CLASS NO:- 10			
TIME / DAY	MON	TUE	WED	THU	FRI	SAT	
09:30-10:30	M-III	ELC	DC	DS	DS	DS	
10:45-11:45	DS	M-III	ELC	DC	M-III	TUT	M-III
12:00-01:00	EXC	DC	M-III	EXC	EXC		ELC

Batches :- S1 :- 01 To 15 S2-16 Onwards

Name Of Subject	TH/PR/OR/TW	Abbreviation	Subject Code	Name of Subject Teacher	Guardian for Roll Nos.
Engineering Maths-III	TH/TW	MIII	207005	Mr. Gunjal S.D.	
Electronic Circuits	TH	EXC	204181	Mr. Tambe. A.T	
Digital Circuits	TH	DC	204182	Mr. Mane A. B.	
Electrical Circuits	TH	ELC	204183	Mr. Andre S.B.	
Data Structure	TH	DS	204184	Miss. Gore S.S.	
Electronic Circuits Lab	PR	EXC Lab	204185	Mr. Tambe. A.T	
Digital Circuits Lab	PR	DC Lab	204186	Mr. Mane A. B.	
Electrical Circuits Lab	TW	ELC Lab	204187	Mr. Andre S.B.	
Data Structure Lab	OR	DS Lab	204188	Miss. Gore S.S.	
Employability Skill Development	TW	ESD	204189	Mr. Choure A.R.	
Mandatory Audit Course 3		MAC-3	204190	Mr. Mane A. B.	

ISSUE	REVISION	PREPARED BY	VERIFIED BY	APPROVED BY	PAGE No	CHAPTER No.
No. : 01	No. : 01	DSGN.: T.T INCHARGE	DSGN.: HOD	DSGN.: IQAC COORDINATOR	01 OF 01	LIST-JCOE-01
DATE: 06/07/20	DATE: 3/08/20	SIGN: <i>T. Tambe</i>	SIGN: <i>[Signature]</i>	SIGN: <i>[Signature]</i>		

Head of Department
(E & TC)
JCEI's Jaihind College of Engineering
Kuran, Tal-Junnar, Dist. Pune - 410531

Principal
JCEI's Jaihind College of Engineering
Kuran, Tal-Junnar, Dist. Pune - 410531

Director
Internal Quality Assurance Cell
JCEI's Jaihind College of Engineering
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 (ACADEMIC YEAR -2020-21)

DEPARTMENT:- Electronics & Telecommunication Engg

CLASS :- S.E. Semester-I		DIV:		SEMESTER:I		W.E.F. :- DATE:06/07/20	
NAME OF CLASS TEACHER:-Mr. Tambe A.T.							
TIME / DAY	MON	TUE	WED	THU	FRI	BLOCK / CLASS NO:-10	
09.30-10:30	M-III	ELC	DC	DS	EXC	SAT	
10:45-11:45	DS	M-III	ELC	DC	M-III TUT	DS	
12.00-01:00	EXC	DC	M-III	EXC	DS	M-III	
Batches :- S1 :-01 To 12							

Name Of Subject	TH/PR/OR/TW	Abbreviation	Subject Code	CLASS/LAB NO.	Name of Subject Teacher	Guardian for Roll Nos.
Engineering Maths-III	TH/TW	MIII	207005		Mr.Gunjal S.D.	
Electronic Circuits	TH	EXC	204181		Mr.Tambe.A.T	
Digital Circuits	TH	DC	204182		Mr.Mane A. B.	
Electrical Ciciuts	TH	ELC	204183		Miss.Andre S.B.	
Data Structure	TH	DS	204184		Mrs.Jadhav S.B.	
Electronic CircuitsLab	PR	EXC Lab	204185		Mr. Tambe.A.T	
Digital Circuits Lab	PR	DC Lab	204186		Mr.Mane A. B.	
Electrical Circuits Lab	TW	ELC Lab	204187		Miss.Andre S.B.	
Data Structure Lab	OR	DS Lab	204188		Mrs.Jadhav S.B.	
Empolybility Skill Development	TW	ESD	204189			
Mandatory Audit Course 3		MAC-3	204190		Mr.Mane A. B.	

ISSUE	REVISION	PREPARED BY	VERIFIED BY	APPROVED BY	PAGE No	CHAPTER No.
No. : 01	No. : 02	DSGN.: T T INCHARGE	DSGN.: HOD	DSGN.: IQAC COORDINATOR	01 OF 01	LIST-JCOE-01
DATE:06/07/20	DATE:6/09/20	SIGN. <i>(Signature)</i>	SIGN. <i>(Signature)</i>	SIGN. <i>(Signature)</i>		

F/JCOE/AC/01/00/1/07/2017

Head of Department
(E & TC)
 JCEI's Jaithind College of Engineering
 Kuran, Tal.Junnar, Dist.Pune - 410511

Director
 Internal Quality Assurance Cell
 JCEI's Jaithind College of Engineering
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INSTITUTE

**TIME TABLE
(ACADEMIC YEAR 2020 -21)**

DEPARTMENT:- Electronics and Telecommunication Engineering

CLASS :- B.E.		DIV:		SEMESTER: I		W.E.F. :- DATE: 6/07/20	
NAME OF CLASS TEACHER:- Mr. Gadekar S.R.				BLOCK / CLASS NO:- 12			
TIME / DAY	MON	TUE	WED	THU	FRI	SAT	
09.30-10:30	EPD	CNS	EPD	CNS	IOT	IOT	
10:45-11:45	VLSI	VLSI	RMT	RMT	VLSI	RMT	
12.00-01:00	IOT	IOT	CNS	EPD	CNS	CNS	

Batches :- B1 :-01 To 15 B2-16 to 35

Name Of Subject	TH/PR/OR/TW	Abbreviation	Subject Code	CLASS/Lab No.	Name of Subject Teacher	Guardian for Roll Nos.
VLSI Design & Technique	TH	VLSI	404181		Mrs.Dhede V.M	
Computer Networks & Security	TH	CNS	404182		Mr.Tambe A.T.	
Radiation & Microwave Techniques	TH	RMT	404183		Mrs.Dhede V.M	
Internet of Things	TH	IOT (EL-I)	404184		Miss.Shinde S.E.	
Electronic Product Design/Artificial Intelligence	TH	EPD(EL-II)	404185		Mr.Gadekar S.R.	
Lab Practice-I (CNS+RMT)	OR/TW	LP-I	404186		Mr.Tambe A.T./ Mrs.Dhede V.M	
Lab Practice-II (VLSI+IOT(EL-I))	PR/TW	LP-II	404187		Mrs.Dhede V.M/ Miss.Shinde S.E.	
Project Phase -I	OR	PP-I	404188		Mr.Gadekar S.R.	
Audit Course V					Mr.Tambe A.T.	CHAPTER No.
ISSUE	REVISION	VERIFIED BY	APPROVED BY			
No.: 01	No.: 02	DSGN.: T T INCHARGE	DSGN.: IQAC COORDINATOR	DSGN.: PRINCIPAL	01 OF 01	LIST-JCOE-01
DATE:06/07/20	DATE:6/09/2020	SIGN: (A.M.)	SIGN:	SIGN:		

F/JCOE/AC/01/00/01/07/2017

Head of Department (E & TC) **Principal**
JCEI's Jaihind College of Engineering, Tal. Junnar, Dist. Pune - 410511

Director
Internal Quality Assurance Cell
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