Savitribai Phule Pune University Faculty of Science & Technology



Course structure

for SE (Electronics/E&TC) (Choice Based Credit System)

(2019 Course)

(With Effect from Academic Year 2020-21)

Savitribai Phule Pune University, Pune S.E. (Electronics/E&TC Engineering) Structure_ 2019 Course (With effect from Academic Year 2020-21)

Semester-III															
Course Code	Course Name	S	eachi chem urs/V)	ie	E	xamiı	nation Ma		Credit						
		Theory	Practical	Tutorial	In-Sem	End-Sem	ML	PR	OR	Total	HI	PR	TUT	Total	
207005	Engineering Mathematics III	04	-	01	30	70	25	-	-	125	04		01	05	
204181	Electronic Circuits	03	-	-	30	70	-	-	-	100	03	-	-	03	
204182	Digital Circuits	03	-	-	30	70	-	-	-	100	03	-	-	03	
204183	Electrical Circuits	03	-	-	30	70	-	-	-	100	03	-	-	03	
204184	Data structures	03	-	-	30	70	-	-	-	100	03	-	-	03	
204185	Electronic Circuit Lab	-	02	-	-	-	-	50	-	50	-	01	- 1	01	
204186	Digital circuits Lab		02					50		50		01		01	
204187	Electrical Circuit Lab	-	02	-	-	-	25	-	-	25	-	01	-	01	
204188	Data Structure Lab	-	02	-	-	-	-	-	25	25	-	01	-	01	
204189	Electronic Skill Development	-	02	-	-	-	25	-	-	25	-	01	-	01	
204190	Mandatory Audit Course 3 *	-	-	-					-	-	-	-	-	-	
	16	10	01	150		75	100	25	700	16	05	01	22		
	1			Seme				<u> </u>		_					
Course Code	Course Name	S	eachin Schem urs/W	e	Examination Scheme and Marks						Credit				
		Theory	Practical	Tutorial	In-Sem	End-Sem	ΤW	PR	OR	Total	ΗT	PR	TUT	Total	
204191	Signals & Systems	03	-	01	30	70	25	-	-	125	03		01	04	
204192	Control Systems	03	-		30	70		-	-	100	03	-	-	03	
204193	Communication Systems	03	-	-	30	70	-	-	-	100	03	-	-	03	
204194	Programming	03	-	-	30	70	-	-	-	100	03	-	-	03	
204195	Signals & Control System Lab		02				50			50		01		01	

204196	Principles of	-	02	-	-	-	-	50	-	50	-	01	-	01
	Communication Systems Lab													
204197	Object Oriented	-	02	-	-	-	-	-	50	50	-	01	-	01
	Programming Lab													
204198	Data Analytics Lab		02				-		25	25		01		01
	Employability Skill	02	02	-	-	-	50	-	-	50	02	01	-	03
	Development													
204200	Project Based Learning [¶]	-	04				50		-	50		02		02
204201	Mandatory Audit Course 4 ^{&}	-	-	-	-	-	-	-	-	-	-	-	-	-
	Total	14	14	01	120	280	175	50	75	700	14	07	01	22
Abbreviations:														
In-Sem: In semester End-sem: End semester							TH	: The	TW : Term Work					
PR : Practical OR : Oral			TUT : Tutorial											
Note: Students of S.E. (Electronics/E&TC) can opt any one of the audit course from the list of audit														
courses prescribed by BoS (Electronics & Telecommunications Engineering)														

- **Instructions:**
 - PR/Tutorial must be conducted in three batches per division.
 - Minimum number of required Experiments/Assignments in PR/ Tutorial shall be carried out as mentioned in the syllabi of respective subjects.
 - Assessment of tutorial work has to be carried out as term-work examination. Term-work Examination at second year of engineering course shall be internal continuous assessment only.
 - η: Project based learning (PBL) requires continuous mentoring by faculty throughout the semester for successful completion of the tasks selected by the students per batch. While assigning the teaching workload of 2 Hrs/week/batch needs to be considered for the faculty involved. The Batch needs to be divided into sub-groups of 5 to 6 students. Assignments / activities / models/ projects etc. under project based learning is carried throughout semester and Credit for PBL has to be awarded on the basis of internal continuous assessment and evaluation at the end of semester.
 - &: Audit course is mandatory but non-credit course. Assessment has to be conducted at the end of Sem III & IV respectively for award of grade at college level. Grade awarded for audit course shall not be calculated for grade point & CGPA.