JAIHIND COMPREHENSIVE EDUCATIONAL INSTITUTE'S

JAIHIND COLLEGE OF ENGINEERING, KURAN

Affiliated to Savitribai Phule Pune University

DTE :- EN6609 SPPU:- CEGP015730

Hon'ble Late Shri Tatyasaheb G
Founder-President

Electronics & Telecommunication Engineering Department

Ref. No. JCEI/ JCOE/E&TC /2024-25/ 03

Date: 06.07.2024

IMPORTANT NOTICE

All B.E. (E&TC) students are hereby inform that submit the 03 topics for project stage-I. The last date for submission **12 July 2024.** Under any circumstances no extension for last date is provided. On 31 July 2023 distribution of Topic for project stage-I will be displayed on student Notice board. The schedule is as follows:

Sr.No.	Activities	Last date
01	Submission of 03 topics	12.07.2023
02	Presentation of all 03 topics(10 min) with DRC committee	16.07.2023
03	Selection of 1 Topic	24.07.2024
04	Presentation of topic (10 min) with RRC committee	29.07.2024
05	Allotment & display of allotted topic with guide name	01.08.2024

Project Stage – I: Project Stage – I is an integral part of the project work. In this, the student shall complete the partial work of the project which will consist of problem statement, literature review, project overview, scheme of implementation (Mathematical Model/SRS/UML/ERD/block diagram/PERT chart, etc.) and Layout & Design of the Set-up. As a part of the progress report of Project work Stage-I, the candidate shall deliver a presentation on the advancement in Technology pertaining to the selected dissertation topic. The student shall submit the duly certified progress report of Project work Stage-I in standard format for satisfactory completion of the work by the concerned guide and head of the Department/Institute.

He

06/07/24